Amendments to the Specification:

Rewrite the paragraph at page 13, line 25 as follows:

With respect to those bits, various observations may be made. First, each of A and B represents an 8-bit value. Second, there are a total of 32 eight-bit values in block ~~22~~ 32, thereby producing a 256-bit PSC which repeats once those 256 bits are transmitted. Third, the PSC from block ~~22~~ 32 is presented by combining two different 8-bit values, namely, A, and B, as well as their respective complements, -A and -B. The values for A and B are as follows:

Rewrite the paragraph at page 24, line 21 as follows:

Looking more specifically to the Hadamard sequence from block 54, recall first by way of contrast that in the Figure 5 embodiment every eighth code from a set of 256-bit codes is selected until a total of 17 total codes have been chosen. In contrast, for block 54, every sixteenth code is selected from the set of 256-bit codes, and here starting at the index ~~vale~~ value of $N=0$. Thus, after the index of $N=0$, each sixteenth code is selected, that is, the additional selections correspond to $N=16$, $N=32$, and so forth whereby the sixteenth code for block 54 corresponds to $N=240$. Further, having selected the 16 codes, for every base station having a code generator block 50 its block 54 is assigned and uses a comma free code consisting of a unique group of 15 codes selected from these 16 codes and ordered in a particular sequence.